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PATENT
Attorney's Do. No. 4591-364

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,813,214 B2
Inventor: Cho et al.
Issued: November 2, 2004

For: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

Ser. No.: 10/693,415 Filed: October 24, 2003
Group Art Unit: 2824 Examiner: Vu A. Le

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing are the following:

- Request for Certificate of Correction
- Certificate of Correction Form PTO 1050 (in duplicate)
- Return postcard
- Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

**Certificate
OCT 28 2005
of Correction**

Respectfully submitted,
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Craig R. Rogers
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22313-1450
Date: October 28, 2005

K. Andrews
Kelly Andrews

OCT 31 2005



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REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
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Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with Applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

At Column 7, Line 30, please replace "transistors M1_O, M2_, M3_O" with --transistors M1_O, M2_O, M3_O--

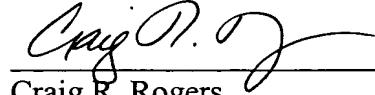
At Column 7, Line 31, please replace "transistor M6_," with --transistor M6_O--

At Column 7, Line 34, please replace "transistors M1_, M2_O" with --transistors M1_O, M2_O--

At Column 13, Line 43, please replace "transistors MS_E" with --transistors M8_E--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,
MARGER JOHNSON & McCOLLOM, P.C.



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Date: October 20, 2005



Kelly Andrews

OCT 31 2005

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,813,214 B2

DATED : November 2, 2004

INVENTOR(S) : Cho et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 7, Line 30, please replace "transistors M1_O, M2_, M3_O" with --transistors M1_O, M2_O, M3_O--

At Column 7, Line 31, please replace "transistor M6_," with --transistor M6_O--

At Column 7, Line 34, please replace "transistors M1_, M2_O" with --transistors M1_O, M2_O--

At Column 13, Line 43, please replace "transistors MS_E" with --transistors M8_E--

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